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38106

7590

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EXAMINER

GIROUX, GEORGE

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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Art Unit: 2183

Continuation of 11: NOTE:

Applicant's arguments filed 26 March 2009 have been fully considered but they are not persuasive.

Applicant argues that the references do not disclose two programmable units each of which has two direct memory access channel interfaces, one on each of two clocks.

However, as described in the rejections, Koch teaches the programmable units, each having direct memory access channel interfaces and using separate clocks. Tang teaches using two direct memory access channel interfaces, and it would have been obvious to one of ordinary skill in the art, at the time the invention was made, to include the dual DMA channel interfaces taught by Tang in each of the programmable units taught by Koch, in connections to the DMAs, including using the clock domain of each associated processor for the associated DMA interfaces, to set up multiple transfers instead of running one transfer at a time, as taught by Tang.

/Eddie P Chan/

Supervisory Patent Examiner, Art Unit 2183